

Abstracts

A Silicon Monolithic Technology for 1-2 GHz Analog Signal Processing

D. Breuer, D. Claxton and A. Cosand. "A Silicon Monolithic Technology for 1-2 GHz Analog Signal Processing." 1976 MTT-S International Microwave Symposium Digest of Technical Papers 76.1 (1976 [MWSYM]): 169-171.

A silicon bipolar monolithic technology has been developed and used for processing analog signals in the 0.5 to 2.0 GHz range. The measured performance of an IQ multiplier phase detector is described as one component of an integrated costas demodulator.

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